



United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/619,351	07/14/2003	Hwa Jeong Lee	CU-3291 RJS	6345
26530 7	590 02/08/2006		EXAM	INER
LADAS & PARRY LLP 224 SOUTH MICHIGAN AVENUE SUITE 1600			SHENG, TOM V	
			ART UNIT	PAPER NUMBER
CHICAGO, II	L 60604		2677	
			DATE MAILED: 02/08/2000	6

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
		10/619,351	LEE ET AL.	
Office Action Summary		Examiner	Art Unit	
	•	Tom V. Sheng	2677	
Period f	The MAILING DATE of this communicator Reply	ation appears on the cover sheet w	ith the correspondence address	
WHIC - Exte afte - If NO - Fail Any	HORTENED STATUTORY PERIOD FOR CHEVER IS LONGER, FROM THE MAI ensions of time may be available under the provisions of 3 or SIX (6) MONTHS from the mailing date of this communi or period for reply is specified above, the maximum statuture to reply within the set or extended period for reply will reply received by the Office later than three months after ned patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF THIS COMMUNION OF	CATION. eply be timely filed ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).	
Status				
1)⊠	Responsive to communication(s) filed	on <u>17 November 2005</u> .		
2a)□	•)⊠ This action is non-final.	•	
3)[3) Since this application is in condition for allowance except for formal matters, prosecution as to the			
	closed in accordance with the practice	under Ex parte Quayle, 1935 C.D	. 11, 453 O.G. 213.	
Disposit	tion of Claims			
_	Claim(s) <u>22-25</u> is/are objected to.	rithdrawn from consideration.		
Applicat	tion Papers			
9)□ 10)⊠	The specification is objected to by the E The drawing(s) filed on 14 July 2003 is Applicant may not request that any objection Replacement drawing sheet(s) including the	/are: a)⊠ accepted or b)⊡ object on to the drawing(s) be held in abeyar he correction is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.121(d).	
Priority :	under 35 U.S.C. § 119			
12)⊠ a)	Acknowledgment is made of a claim for All b) Some * c) None of: 1. Certified copies of the priority do	ocuments have been received. Ocuments have been received in A the priority documents have been Il Bureau (PCT Rule 17.2(a)).	pplication No received in this National Stage	
Attachmen		_		
2) 🔲 Notic 3) 🔲 Infor	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO rmation Disclosure Statement(s) (PTO-1449 or PT er No(s)/Mail Date	9-948) Paper No(s	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152)	

Application/Control Number: 10/619,351 Page 2

Art Unit: 2677

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of claims 1-3 and 21-25 in the reply filed on 11/17/2005 is acknowledged. Applicant considers claim 1 to be generic to both species 1 and 2. The Examiner disagrees because claim 1 is directed to the use of a PWM signal, which is a feature of species 1 but not species 2. Species 2 does not use PWM signal but a digital signal that is subsequently converted to an analog common voltage signal.

Claim Objections

2. Claims 2-3 and 22-23 are objected to because of the following informalities: claims 2 and 3 define third, fourth and fifth resistors without defining first and second resistors first. Similarly, claims 22 and 23 define eighteenth, nineteenth, twentieth resistors and sixth capacitor without defining any preceding resistors or capacitors first. Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Application/Control Number: 10/619,351

Art Unit: 2677

4. Claims 1-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Didier et al. (US 6,236,384 B1) in view of Furukawa et al. (US 6,388,967 B2) and Willis (US 2002/0097206 A1).

As for claim 1, Didier teaches a voltage regulating circuit of a liquid crystal display device, comprising:

a pulse signal generating means (microcontroller 12; fig. 1) for outputting a pulse width modulation signal (PWM output 20) in response to up/down signal for adjusting a voltage (viewing angle of LCD can be adjusted manually or automatically via an analog bias voltage; column 2 lines 38-56);

a smoothing means (filter 22) for smoothing the pulse width modulation signal from the pulse signal generating means to a direct current level (the filter's RC arrangement shows a low-pass filter and would produce a substantially DC signal; column 2 line 56 through column 3 line 5); and

an amplifying means (buffer amplifier 24) for outputting a voltage signal (a buffered bias voltage to the liquid crystal display via a viewing angle control circuit 14).

However, Didier does not teach the amplifying means (buffer amplifier 24) for amplifying the signal smoothed by the smoothing means to a predetermined level and outputting a voltage signal. Furukawa teaches with respect to LCD display that as the amplitude range of a PWM signal (from a PWM signal generator) is small, it is possible to adjust the total gain of the signal by disposing an amplifier at the front or rear stage of a filter 103 (fig. 1; column 6 lines 41-47). It would have been obvious to one of ordinary skill in the art to at least incorporate an amplification factor in Didier's buffer amplifier 24.

Application/Control Number: 10/619,351 Page 4

Art Unit: 2677

since this would allow the generation of a smaller PWM signal. Moreover, the idea of amplifying a voltage to a desirable value or range is common.

Still, Didier and Furukawa do not teach that the bias voltage signal is a common voltage signal. Willis teaches in liquid crystal display of biasing the common voltage by with a substantially DC voltage (fig. 1 and 2; page 1 paragraph 14 through page 2 paragraph 18). It would have been obvious to one of ordinary skill in the art to at least drive the bias voltage to the common electrode of Didier, as changes in the common voltage would provide a bias in display and thus the viewing angle adjustment.

As for claim 2, the resistor R and capacitor C of the filter 22 of Didier correspond to claimed third resistor and first capacitor, respectively.

As for claim 3, the arrangement of a fourth and fifth resistor at an amplifier as claimed is well known in order to setup a gain factor.

Allowable Subject Matter

- 5. Claim 21 is allowed.
- 6. Claims 24 and 25 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

Application/Control Number: 10/619,351 Page 5

Art Unit: 2677

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tom V. Sheng whose telephone number is (571) 272-7684. The examiner can normally be reached on 9:00am - 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tom Sheng February 1, 2006

AMR A. AWAD
PRIMARY EXAMINED

Amy Musik Misser